CERTIFICATE
OF REGISTRATION

This is to certify that the management system of:

System Level Solutions (India) Private Limited

Main Site: Plot 32, Zone D-4, Phase-I, GIDC Estate, Vitthal Udyog Nagar,

Dist. Anand-388121, Gujarat, India

See appendix for additional sites and additional site scopes

has been registered by Intertek as conforming to the requirements of:

ISO 9001:2015

The management system is applicable to:

1. Design and Development of Enterprise class software covering applications like web, mobile, cloud, test, IoT, (Internet of Things), & embedded.
2. Design, Development, and Manufacturing of Products (i.e., Software & Hardware) covering applications for Electric Vehicle, IoT, FPGA/CPLD (Field Programmable Gate Array / Complex Programmable Logic Device), Test and Measurement, Home Automation, Street Light, Transformer Monitoring.

Certificate Number:
IVA-0423.12

Initial Certification Date:
20 May 2021

Last Certificate Expiry Date:
01 May 2024

Date of Last Recertification Audit:
17 April 2024

Certification Cycle Start Date:
02 May 2024

Issuing Date:
06 May 2024

Valid Until:
01 May 2027

ISO 9001:2015 CERTIFICATION ™ intertek

UKAS
MANAGEMENT
SYSTEMS
014

Calin Moldovean
President, Business Assurance

Intertek Certification Limited, 10A Victory Park, Victory Road, Derby DE24 8ZF, United Kingdom

Intertek Certification Limited is a UKAS accredited body under schedule of accreditation no. 014.

CERTIFICATE
OF REGISTRATION

APPENDIX

This appendix identifies the locations covered by the management system of:

System Level Solutions (India) Private Limited

This appendix is linked to the Main Certificate # IVA-0423.12 and cannot be shown nor reproduced without it.

System Level Solutions (India) Private Limited

Plot 32, Zone D-4, Phase-I, GIDC Estate, Vitthal Udyog Nagar,
Dist. Anand-388121, Gujarat, India

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System Level Solutions (India) Private Limited

Cube 675, Second Floor 201, Third Floor 302, Fourth Floor 401, Near Sardar Patel Statue, Vallabh Vidyanagar - 388120, Dist. Anand, Gujarat, India.

1. Design and Development of IP core for FPGA/ASIC (Field Programmable Gate Array / Application Specific Integrated Circuit),
2. Design and Development of Enterprise class software covering applications like web, mobile, cloud, test, IoT, and embedded